

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION:      Frampton E. ELLIS III      Conf. No.      9048  
SERIAL NUMBER:      09/935,779      Group:      2153  
FILED:      August 24, 2001      Examiner:      Sean M. Reilly  
FOR:      GLOBAL NETWORK COMPUTERS

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Assistant Commissioner for Patents  
PO BOX 1450  
ALEXANDRIA, VA 22313-1450

December 12, 2006

Sir:

Applicant(s) wish(es) to disclose the following information.

REFERENCES

- ☒ Applicant(s) wish(es) to make of record the documents listed on the attached Form PTO-1449. Copies of the listed documents are attached, where required, as are either statements of relevancy or any readily available full or partial English translations of any non-English-language documents.

RELATED CASES

- ☐ Attached is a list of Applicant's(s') pending applications and issued patents which may be related to the present application. Copies of the documents, where required, are attached along with Form PTO-1449.

CERTIFICATION

The undersigned certifies that

- ☐ each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign or international patent office in a counterpart foreign or international application for the first time (to the knowledge of the undersigned, having made reasonable inquiry) not more than three months prior to the filing of this statement (See 37 CFR 1.97(e)(1)).
- ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign or international patent office in a counterpart foreign or international application, and, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement. (See 37 CFR 1.97(e)(2))

BASIS FOR CONSIDERATION

This Information Disclosure Statement is filed:

- ☐ without fee and within three months of the filing date of the application.
- ☐ without fee and within three months of the date of entry of the U.S. national stage.
- ☐ without fee and before the mailing date of a first Office Action on the merits (to the knowledge of the undersigned).
- ☐ without fee and with the appropriate certification above.
- ☐ without fee and with a new CPA application.
- ☐ without fee and with a Request for Continued Examination.
- ☒ with fee and before the mailing date of any Final Office Action, Notice of Allowance or an action that otherwise closes prosecution (to the knowledge of the undersigned).
- ☐ with fee, appropriate certification above, and before payment of the Issue Fee.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 50-3266.

Respectfully submitted,

DLA PIPER US LLP



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LKN/mrh

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

**Complete if Known**

Application Number	09/935,779
Filing Date	August 24, 2001
First Named Inventor	Frampton E. ELLIS III
Art Unit	2153
Examiner Name	Sean M. Reilly
Attorney Docket Number	313449-P0008

Sheet	1	of	3
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Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US -4,245,306	1/1/1981	Besemer et al.	
		US -4,278,837	7/1/1981	Best	
		US -4,747,139	5/1/1988	Taaffe	
		US -5,237,507	8/17/1993	Chasek	
		US -5,283,819	2/1994	Glick et al.	
		US -5,535,408	7/9/1996	Hillis	
		US -5,587,928	12/1996	Jones et al.	
		US -5,588,003	12/24/1996	Ohba et al.	
		US -5,734,913	3/1998	Iwamura et al.	
		US -5,872,987	2/1999	Wade et al.	
		US -5,917,629	6/29/1999	Hortensius et al.	
		US -6,067,082	5/2000	Enmei	
		US -6,167,428	12/26/2000	Ellis	
		US -5,260,943	11/1993	Comroe et al.	
		US -5,615,127	3/1997	Beatty et al.	
		US -5,909,052	6/1/1999	Ohta et al.	
		US -5,600,597	2/1/1997	Kean et al.	
		US -5,809,190	9/1/1998	Chen	

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>2</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
Examiner Signature				Date Considered		

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Substitute for form 1449A/PTO			<b>Complete if Known</b> <sup>A</sup>		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)			Application Number	09/935,779	
			Filing Date	August 24, 2001	
			First Named Inventor	Frampton E. ELLIS III	
			Art Unit	2153	
			Examiner Name	Sean M. Reilly	
Sheet	2	of	3	Attorney Docket Number	313449-P0008

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Waltz et al., "Make 'em pay: Billing Net Usage", MacWeek, Vol. 6 (No. 27), p. 24 (2) (Dialog full text), (July 27, 1992).	
		"The Economics of Network Billing: Network Billing and Monitoring Systems Can Improve Efficiency and Cut Costs", IBM System User, Vol. 14 (No. 11), p. 53 (1) (Dialog Fulltext), (Nov., 1993).	
		"Let Your Computer Make Money While You Sleep", Newsbytes, p. 1 (Dialog Fulltext), (August 16, 1996).	
		Regenold et al., "A Single-Chip Multiprocessor DSP Solution for Communication Applications", ASIC Conference and Exhibit 1994, pp. 437-440.	
		Geppert, L., "Solid State [Trend/Development], IEEE Spectrum, Vol. 33, Issue 1, 1996, pp. 51-55.	
		Li, Yao, "Free-Space Optical Bus-based WDMA Interconnects for Parallel Computation", LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, pp. 588-589, November 16-19, 1992.	
		Dickinson et al., "An Integrated Free Space Optical Bus", 1989 IEEE International Conference on Computer Design, VLSI In Computers and Processors, pp. 62-65, October 2-4, 1989.	
		Natarajan et al., "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor", Journal of Lightwave Technology, Vol. 13, No. 6, pp. 1031-1040, June 6, 1995.	
		Zhao et al., "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems", Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, pp. 188-195, October 23-24, 1995.	
		Wu et al., "Microprocessor Control Signal Transmission Through Optical Fiber", Conference Record of 1992, IEEE Industry Applications Society Annual Meeting, pp. 1747-1750, October 4-9, 1992.	
Examiner Signature			Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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		NONE, "PC Vision: Intel unveils plans to bring PCs to Vehicles"; EDGE: Work-Group Computing Report, EDGE Publishing, pp. 1-2 (October 28, 1996).	
		The American Heritage College Dictionary 4th Ed. - definition of "firewall".	
		White, "Covert Distributed Processing with Computer Viruses", Advances in Cryptology, Crypto 89, Springer LNCS, Vol. 435, pp. 616-619.	
		Wilkinson et al., "Parallel Programming", Prentice Hall, 1998.	
		Slater, "The Microprocessor Today", IEEE Micro 1996, pp. 32-44.	
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